Lily (Yueting) Li

li-yueting.github.io | yueting.li.1230@gmail.com | 650-441-7953

Education

- EECS PhD student at UC Berkeley 2023 – Now
- MS in EE at Stanford University 2021 – 2023
- BS in EE at Huazhong University of Sci & Tech (HUST) 2016 – 2020

Research Experience

- **Research Assistant, Chien Lab, UC Berkeley** 08/2023 – Now
  Advisor: Jun-Chau Chien
  - Developing SEMI-IDC, an all-electronic, non-optics impedance cell deformability cytometry (IDC) integrated into a portable microfluidics system powered by the low-cost, millimeter-sized semiconductor (SEMI) chip technology
  - Working on the inverse design of RF devices – filters, couplers, splitters, and antennas – essential for telecom and sensing technologies. Tapeout is scheduled to be in March 2025.
- **Research Assistant, Murmann Mixed-Signal Group, Stanford University** 02/2022 – 06/2023
  Advisor: Boris Murmann
  - Led and developed the first open-source analog layout automation flow using the digital PnR tool and the above analog standard cell library [Code] [Slides].
  - Taped out a bandgap reference circuit using our analog layout automation flow with SkyWater 130nm technology and open-source tools Magic, Netgen, Xschem and Mflowgen.
- **Research Assistant, Nano Device Lab, National University of Singapore** 10/2019 – 11/2019
  Advisor: Aaron Thean
  - Worked on developing Bluetooth Low Energy biomedical wearable sensor monitoring stress, glucose et al
  - Designed mobile software application for BLE biomedical wearable sensor using Android Studio.

Skills

Python, Verilog, TCL, Shell, MATLAB, YAML, C++, Lua, R, PyTorch, TensorFlow, Virtuoso, Innovus, Calibre, Synopsys VCS, Linux

Courses

Fundamentals of Analog Integrated Circuit Design (EE214A), Introduction to VLSI Systems (EE271), Advanced Integrated Circuit Design (EE214B), Computer Systems Architecture (EE282), Design Projects in VLSI Systems I (EE272), Design Projects in VLSI Systems II (EE372), Advanced Topics in Power Electronics EE254), Biochips and Medical Imaging (EE254).

Publication

- "An RFID-Inspired One-Step Packaged Multimode Bio-Analyzer with Vacuum Microfluidics for Point-of-Care Diagnostics" Yan-Ting Hsiao, Ya-Chen Tsai , Wei Foo, Hong-Yu Hou, Yun-Chun Su, Yueting Li, Jun-Chau Chien, ISSCC 2025 (to appear).
- "Joint Graph Convolution for Analyzing Brain Structural and Functional Connectome" Yueting Li, Qingyue Wei, Eshan Adeli, Kilian Pohl, Qingyu Zhao, MICCAI 2022. [Paper][Code][Video]
- "Deconvolutional Networks on Graph Data" Jia Li, Jiajin Li, Yang Liu, Jianwei Yu, Yueting Li, Hong Cheng, NeurIPS 2021. [Paper]

Internship

- **VLSI Design Methodology Intern at Nvidia** 05/2024 – 08/2024
  - Worked on enhancing the placement automation for customized circuits as part of a research-to-product project.
  - The co-authored paper was selected for oral presentation at NTECH, which is the internal conference at Nvidia, and its acceptance rate is 22%.

Teaching

TA of Course Interconnection Networks (EE382C), Stanford University 01/2022 – 03/2022
TA of Course Introduction to Photonics (EE134), Stanford University 09/2021 – 12/2021
TA of undergraduate summer AI course at the National University of Singapore 08/2019
Volunteer Teaching in the rural senior high at Enshi, Hubei, China 08/2018

Honors & Awards

MICCAI 2022 Travel Award 08/2022
Outstanding Undergraduate Award at HUST 06/2020
Arts and Sports Scholarship, HUST 5/2017, 11/2017
Volunteer Service, Students' International Communication Association, HUST 09/2017 – 12/2017